

# IAP300T120 Integrated Advanced PowerStack

300A/1200V 3-phase Bridge IGBT Inverter



#### **FEATURES INCLUDE**

- Multi-Function Power Assembly
- Compact Size 8.00" H X 11.00" W X 17.56" D
- DC Bus Voltages to 800VDC
- Snubber-less operation to 650VDC
- Switching frequencies to over 20kHz
- Protective circuitry with fail-safe opto-isolated fault annunciation, including:
  - OverCurrent
- Short circuit
- OverVoltage
- P.S. UnderVoltage
- OverTemperature
- Opto-isolated or fiber-optic gate drive and fault signal output for electrical isolation and noise immunity
- Integrated cooling with temperature sensors and feedback
- Many options Diode/SCR Front End, etc.

### **Configurable Power**

The IAP-300T120 PowerStack is a flexible, highly integrated IGBT based power assembly with a wide range of applications. These include inverters for renewable energy, energy storage, motor controls, switch mode power supplies (SMPS), UPS, welders, etc. IAP300T120 PowerStack can be operated at frequencies to over 20kHz. The IAP300T120 PowerStack is configured as a three-phase bridge inverter mounted on an air-cooled or liquid-cooled heat sink. Configurations include options for (full, half or no control) converter input circuitry, inverter output circuitry, cooling and a wide variety of drivers and safety features for the converter front end and IGBT inverter output stage.

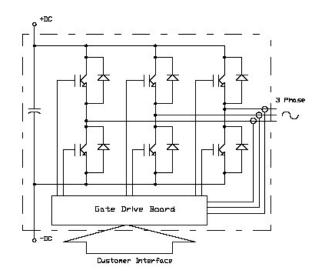
To operate at high switching frequencies, the IAP300T120 PowerStack utilizes a low inductance laminated bus structure, optically isolated or fiber optically coupled gate drive interfaces, isolated gate power supplies and a DC-link capacitor bank.

The IAP300T120 PowerStack provides built in protection features including: OverVoltage, UnderVoltage lockout, OverCurrent, OverTemperature, short circuit and optional airflow or liquid flow indicators.

Flexibility is a key feature of the IAP300T120 PowerStack. Options include: a choice of converter front ends, rectifier, half or full SCR control, with or without SCR gate firing boards and soft-start circuitry. A choice of cooling methods, forced air or liquid is also available. Customer provided PWM is optically coupled or a fiber optic link can be provided to the IGBT interface. Current feedback is provided by Hall effect current transducers.

The IAP300T120 PowerStack is rated to maximum input voltages up to 800 VDC, switching frequencies to over 20kHz, includes many safety features to protect the IGBTs and output circuitry and can be configured to meet your application.

## Schematic:



Rev. 08012017

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### IAP300T120 PowerStack

Absolute Maximum Ratings – T <sub>J</sub> =25°C unless otherwise specified						
General	Symbol	Value	Units			
IGBT Junction Temperature	TJ	-40 to +150	°C			
Storage Temperature	Tstg	-40 to +125	°C			
Operating Temperature	Top	-25 to +85	°C			
Voltage applied to DC terminals	Vcc	800	Volts			
Isolation voltage, AC 1 minute, 60Hz sinusoidal	V <sub>ISO</sub>	2500	Volts			
IGBT Inverter						
Collector Current (T <sub>C</sub> =25°C)	Ic	300	Amperes			
Peak Collector Current (TJ<150°C)	Ісм	600	Amperes			
Emitter Current	lε	300	Amperes			
Peak Emitter Current	I <sub>EM</sub>	600	Amperes			
Maximum Collector Dissipation (TJ<150°C)	Pcd	1600	Watts			
Gate Drive Board						
Unregulated +24V Power Supply		30	Volts			
Regulated +15V Power Supply		18	Volts			
PWM Signal Input Voltage (Factory Settable from 3.3 – 15)		15	Volts			
Fault Output Supply Voltage		30	Volts			
Fault Output Current		50	mA			

### IGBT Inverter Electrical Characteristics, T<sub>J</sub>=25°C unless otherwise specified

Parameter	Symbol	<b>Test Conditions</b>	Min	Тур	Max	Units
Collector Cutoff Current	I <sub>CES</sub>	V <sub>CE</sub> =V <sub>CES</sub> , V <sub>GE</sub> =0V	-	-	1	mA
Collector-Emitter Saturation Voltage	V	Ic=300A, TJ=25°C	-	1.75	2.15	Volts
Collector-Emitter Saturation voltage	V <sub>CE(sat)</sub>	Ic=300A, T <sub>J</sub> =125°C	-	2.0	-	Volts
Emitter-Collector Voltage	V <sub>EC</sub>	IE=300A	-	-	3.2	Volts
	t <sub>d(on)</sub>		-	-	200	ηs
Industive Load Cuitabing Times	t <sub>r</sub>	V <sub>CC</sub> =600V	-	-	45	ηs
Inductive Load Switching Times	t <sub>d(off)</sub>	I <sub>C</sub> =300A	-	-	500	ηs
	t <sub>r</sub>	V <sub>GE</sub> =15V	-	-	100	ηs
Diode Reverse Recovery Time	Trr	$R_G=1.8\Omega$	-	-	250	ηs
Diode Reverse Recovery Charge	Qrr		-	30.0	-	μC
DC Link Capacitance			-	4950	-	μF

### **Thermal and Mechanical Parameters**

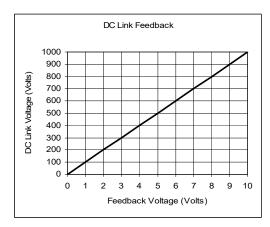
Parameter	Symbol	<b>Test Conditions</b>	Min	Тур	Max	Units
IGBT Thermal Resistance, Junction to Case	R <sub>Θ(j-c)</sub>	Per IGBT ½	-	-	0.093	°C/W
		module				
FWD Thermal Resistance, Junction to Case	R <sub>Θ(i-c)</sub>	Per FWD ½	-	-	0.15	°C/W
	- 0 /	module				
Heatsink Thermal Resistance	R⊝(s-a)	1500 LFM airflow	-	.045	-	°C/W
Mounting Torque, AC terminals			-	75	90	In-lb
Mounting Torque, DC terminals			-	130	150	In-lb
Mounting Torque, case mounting			-	130	150	In-lb
Weight			-	21	-	lb

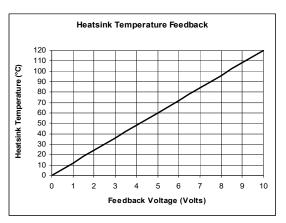


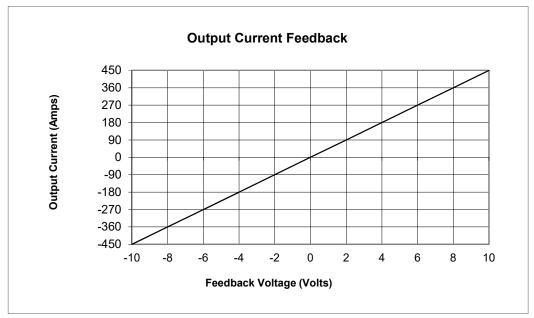
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#### **Gate Drive Board Electrical Characteristics**

Parameter	Min	Тур	Max	Units
Unregulated +24V Power Supply	20	24	30	Volts
Regulated +15V Power Supply	14.4	15	18	Volts
PWM Input On Threshold	12	15	-	Volts
PWM Input Off Threshold	-	0	2	Volts
Output OverCurrent Trip	-	450	-	Amperes
OverTemperature Trip	98	100	102	°C
OverVoltage Trip	-	900	-	Volts
DC Link Voltage Feedback	See F	igure B	elow	Volts
Heatsink Temperature Feedback	See Figure Below		Volts	
Output Current Feedback	See F	igure B	elow	Volts







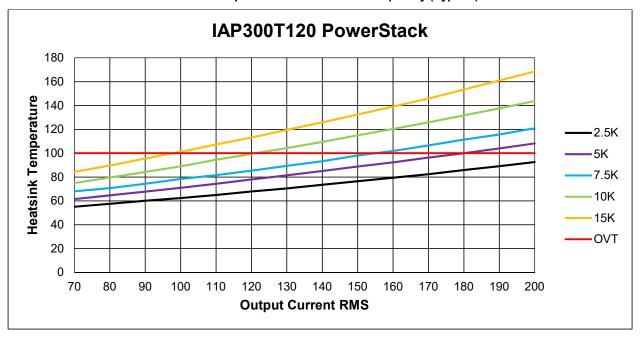
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#### **Performance Curves**

**Effective Output Current vs. Carrier Frequency (Typical)** 



Conditions	Symbol	Value	Units		
Ambient Temperature	T <sub>A</sub>	40	Ô		
DC Bus Voltage	V <sub>CC</sub>	600	Volts		
Load Power Factor	COS Φ	0.8			
IGBT Saturation Voltage	V <sub>CE(sat)</sub>	Typical @ Tյ=125°C	Volts		
IGBT Switching Loss	Esw	Typical @ Tյ=125°C	mJ		
Airflow	-	1500	LFM		
Switching Conditions	3-ph	3-phase PWM, 60Hz sinusoidal output			

### Options for the IAP300T120-XX

				Optio	n Numb	er			
Option	01	02	03	04	05	06	07	08	09
Blower	Х		Х		Х		Х		Х
Half-Control SCR Converter		Х	Х						
Full Control SCR Converter				Х	Х				
Diode Converter						Х	Х		
Dual Inverter								Х	Х

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## IAP300T120 Integrated Advanced PowerStack 300A/1200V 3-phase Bridge IGBT Inverter

#### Interface

Pin#	Signal Name	Description		
1	Shield	Connected to circuit ground		
2	PWM A-	0-15V signal controlling the duty cycle of A- IGBT		
3	Phase A Error <sup>1</sup>	Open collector output, external pull-up resistor required.		
		LOW=No Error; Floating=Phase A overcurrent or short circuit		
4	PWM A+	0-15V signal controlling the duty cycle of A+ IGBT		
5	PWM B-	0-15V signal controlling the duty cycle of B- IGBT		
6	Phase B Error <sup>1</sup>	Open collector output, external pull-up resistor required.		
		LOW=No Error; Floating=Phase B overcurrent or short circuit		
7	PWM B+	0-15V signal controlling the duty cycle of B+ IGBT		
8	PWM C-	0-15V signal controlling the duty cycle of C- IGBT		
9	Phase C Error <sup>1</sup>	Open collector output, external pull-up resistor required.		
		LOW=No Error; Floating=Phase C overcurrent or short circuit		
10	PWM C+	0-15V signal controlling the duty cycle of C+ IGBT		
11	OverTemp <sup>1</sup>	Open collector output, external pull-up resistor required.		
		LOW=No Error; Floating=Heatsink overtemp		
12	Not Connected			
13	DC Link Voltage	Analog voltage representation of DC link voltage		
14	24VDC Input Power <sup>2</sup>	20-30VDC input voltage range		
15	24VDC Input Power <sup>2</sup>	20-30VDC input voltage range		
16	15VDC Input Power <sup>2</sup>	14.4-18VDC input voltage range		
17	15VDC Input Power <sup>2</sup>	14.4-18VDC input voltage range		
18	GND	Ground reference for 15 and 24VDC inputs		
19	GND	Ground reference for 15 and 24VDC inputs		
20	Heatsink Temperature	Analog voltage representation of heatsink temperature		
21	GND <sup>3</sup>	Tied to pins 18 and 19		
22	I <sub>OUT</sub> Phase A	Analog voltage representation of phase A output current		
23	GND <sup>3</sup>	Tied to pins 18 and 19		
24	I <sub>OUT</sub> Phase B	Analog voltage representation of phase B output current		
25	GND <sup>3</sup>	Tied to pins 18 and 19		
26	I <sub>OUT</sub> Phase С	Analog voltage representation of phase C output current		

### NOTES:

1. Open collectors can be pulled up to 30VDC Max and sink 50mA continuous.

 DO NOT connect a 15VDC and 24VDC source to the unit at the same time. Use one or the other.

3. GND signals to be used for analog feedback signals, i.e. twisted pair with I<sub>OUT</sub> Phase A.

### **Gate Drive Interface Connector**

Description	Symbol	Туре	Manufacturer
Gate Drive Interface Header	J1	0.100" x 0.100" latching header, 26 pin	3M #3429-6002 or equivalent
Recommending Mating Socket	-	0.100" x 0.100" IDC socket, 26 pin	3M #3399-7600 or equivalent
Recommended Strain Relief	-	Plastic strain relief	3M #3448-3026 or equivalent

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# Mechanical Information

